





Parameter	Ratings	Units
Blocking Voltage	350	$V_P$
Load Current	120	mA
Max R <sub>ON</sub>	35	Ω

#### **Features**

- Low Drive Power Requirements (TTL/CMOS Compatible)
- · Arc-Free With No Snubbing Circuits
- 3750V<sub>rms</sub> Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- · Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Packages Available

# **Applications**

- · Telecom Switching
  - Tip/Ring Circuits
  - · Modem Switching (Laptop, Notebook, Pocket Size)
  - · Hook Switch
  - Dial Pulsing
  - Ground Start
  - · Ringing Injection
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - · Electronic Switching
  - I/O Subsystems
  - · Meters (Watt-Hour, Water, Gas)
  - · Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

## **Description**

LAA110 is a Dual 1-Form-A Solid State Relay that has two independently controlled optically coupled MOSFET switches. The MOSFET switches and photovoltaic die use Clare's patented OptoMOS® architecture to provide 3750 V<sub>rms</sub> of input to output isolation. The optically coupled output is controlled by a highly efficient GaAlAs infrared LED. This dual pole OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications and saves board space by incorporating both switches in a single 8-Pin package.

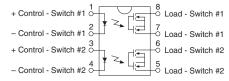
# **Approvals**

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10
- EN/IEC 60950-1:2001 Compliant

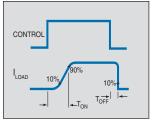
# **Ordering Information**

Part #	Description
LAA110	8-Pin DIP (50/Tube)
LAA110S	8-Pin Surface Mount (50/Tube)
LAA110STR	8-Pin Surface Mount (1,000/Reel)
LAA110P	8-Pin Flat Pack (50/Tube)
LAA110PTR	8-Pin Flat Pack (1,000/Reel)

# **Pin Configuration**



# Switching Characteristics of Normally Open (Form A) Devices











# **Absolute Maximum Ratings**

Parameter	Ratings	Units	
Blocking Voltage	350	$V_{P}$	
Reverse Input Voltage	5	V	
Input Control Current	50	mA	
Peak (10ms)	1	Α	
Input Power Dissipation <sup>1</sup>	150	mW	
Total Power Dissipation <sup>2</sup>	800	mW	
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>	
Operational Temperature	-40 to +85	°C	
Storage Temperature	-40 to +125	°C	

<sup>&</sup>lt;sup>1</sup> Derate Linearly 1.33 mw/°C

Electrical absolute maximum ratings are at 25°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

# **Electrical Characteristics**

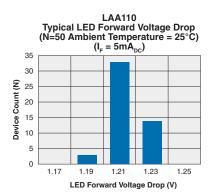
Parameter	Conditions	Symbol	Min	Тур	Max	Units		
Output Characteristics @ 25°C								
Load Current, Continuous <sup>1</sup>	-	IL	-	-	120	mA		
Peak Load Current	t=10ms	I <sub>LPK</sub>	-	-	350	mA		
On-Resistance	I <sub>L</sub> =120mA	R <sub>ON</sub>	-	25	35	Ω		
Off-State Leakage Current	V <sub>L</sub> =350V	I <sub>LEAK</sub>	-	-	1	μΑ		
Switching Speeds								
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	T <sub>ON</sub>	-	-	3	ms		
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	T <sub>OFF</sub>	-	-	3	ms		
Output Capacitance	50V; f=1MHz	C <sub>OUT</sub>	-	25	-	pF		
Input Characteristics @ 25°C					,			
Input Control Current	I <sub>L</sub> =120mA	I <sub>F</sub>	-	-	5	mA		
Input Dropout Current	-	-	0.4	0.7	-	mA		
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V		
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μА		
Common Characteristics @ 25°C								
Input to Output Capacitance	-	C <sub>I/O</sub>	-	3	-	pF		

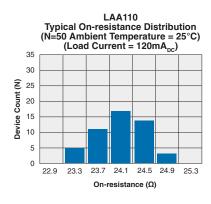
 $<sup>^{1}\,</sup>$  If both poles operate the load current must be derated so as not to exceed the package power dissipation value.

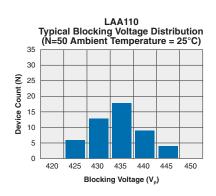
<sup>&</sup>lt;sup>2</sup> Derate Linearly 6.67 mw/°C

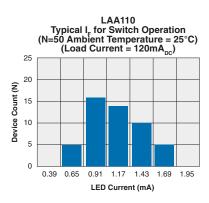


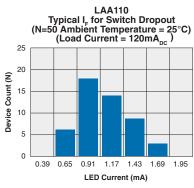
## **PERFORMANCE DATA\***

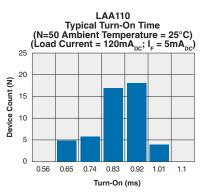


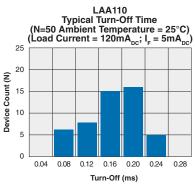


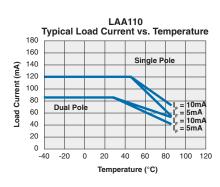


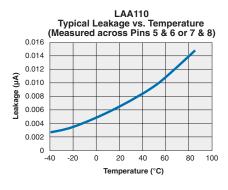


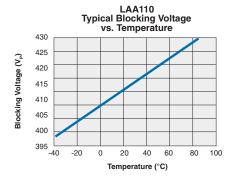


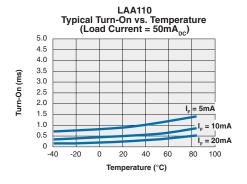


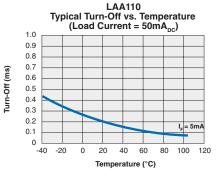








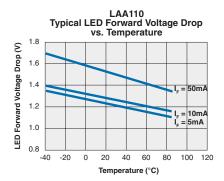


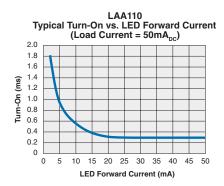


<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

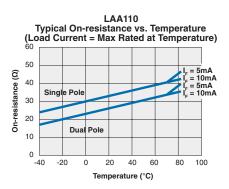


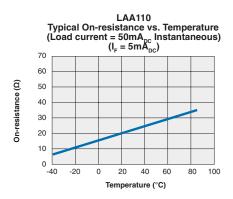
#### **PERFORMANCE DATA\***

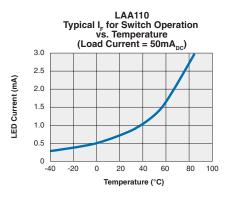


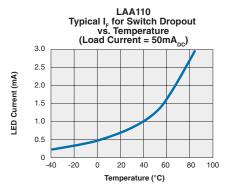


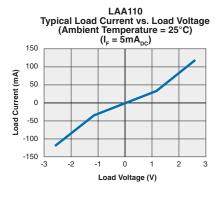


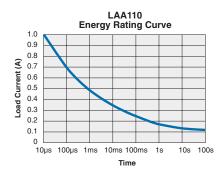












<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



# **Manufacturing Information**

## Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

Recommended soldering processes are limited to 260°C component body temperature for 10 seconds.

#### Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

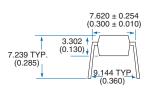


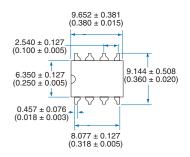


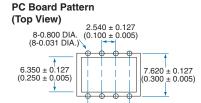


#### MECHANICAL DIMENSIONS

#### 8-Pin DIP Through Hole (Standard)

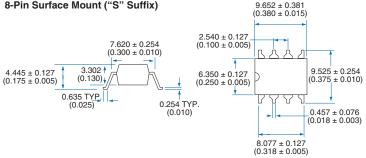


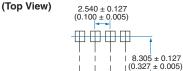




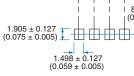
7.620 ± 0.127 (0.300 ± 0.005)

#### 8-Pin Surface Mount ("S" Suffix)

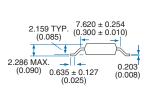


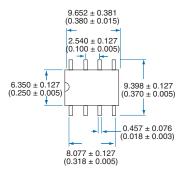


**PC Board Pattern** 

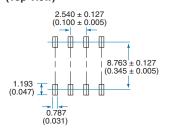


## 8-Pin Flatpack ("P" Suffix)





#### PC Board Pattern (Top View)

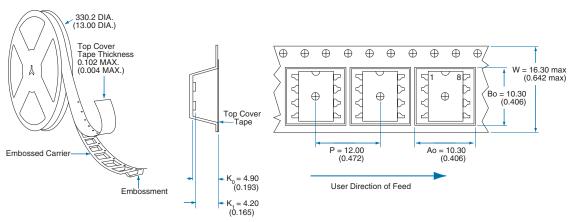


**Dimensions** mm (inches)



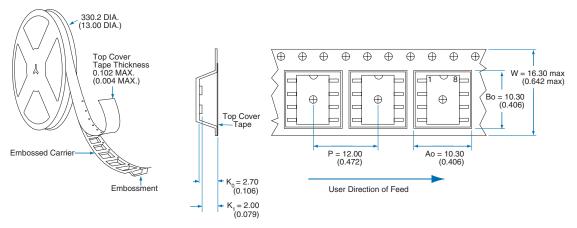
#### **MECHANICAL DIMENSIONS**

#### Tape and Reel Packaging for 8-Pin Surface Mount Package



NOTE: Tape dimensions not shown, comply with JEDEC Standard EIA-481-2

#### Tape and Reel Packaging for 8-Pin Flatpack Package



 $\textbf{NOTE:} \ \ \mathsf{Tape} \ \mathsf{dimensions} \ \mathsf{not} \ \mathsf{shown}, \ \mathsf{comply} \ \mathsf{with} \ \mathsf{JEDEC} \ \mathsf{Standard} \ \mathsf{EIA-481-2}$ 

Dimensions mm (inches)

#### For additional information please visit our website at: www.clare.com

Clare, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.