

Current Transducer LTSR 6-NP

For the electronic measurement of currents: DC, AC, pulsed, mixed, with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

$I_{PN} = 6 At$









	160/3		
Ele	ectrical data		
I _{PN}	Primary nominal r.m.s. current	6	At
P	Primary current, measuring range	0 ± 19.2	¹⁾ At
PDC	Overload capability	250	At
OUT	Analog output voltage @ I _p	$2.5 \pm (0.625)$	5· I _P / I _{PN}) V
001	$\mathbf{I}_{p} = 0$	2.5 2)	· · · · V
REF	Voltage reference (internal reference), refout mode	e 2.5 ³⁾	V
	Voltage reference (external reference), refin mode	1.9 2.7 4)	V
l _s	Number of secondary turns (± 0.1 %)	2000	
₹ _	Load resistance	≥ 2	kΩ
, L	Max. capacitive loading	500	pF
IM	Internal measuring resistance (± 0.5 %)	208.33	Ω
CR	Thermal drift of R	< 50	ppm/K
' _C	Supply voltage (± 5 %)	5	V
	Current consumption @ $V_c = 5 \text{ V}$ Type	$28 + I_S^{5} + (V_O)$	л /R _) m А
Ac	curacy - Dynamic performance data		
(Accuracy @ I_{PN} , $T_{A} = 25^{\circ}C$	±0.2	%
	Accuracy with $\mathbf{R}_{\text{IM}} @ \mathbf{I}_{\text{PN}}$, $\mathbf{T}_{\text{A}} = 25^{\circ}\text{C}$	±0.7	%
L	Linearity error	< 0.1	%
		Max	
CV	Thermal drift of $\mathbf{V}_{\text{OUT}}/\mathbf{V}_{\text{REF}} \otimes \mathbf{I}_{\text{P}} = 0$		
- 001	- 40°C + 85°C	C 150	ppm/K
CE ₆	Thermal drift of the gain - 40°C + 85°C		ppm/K
, OM	Residual voltage @ $I_p = 0$, after an overload of $3 \times I_p$		mV
OIVI	5 x I _p		mV
	10 x I _p		mV
CV	Thermal drift of internal $\mathbf{V}_{REF} @ \mathbf{I}_{P} = 0 - 10^{\circ}\mathrm{C} + 85^{\circ}$	N	ppm/K
KEF	- 40°C 10		ppm/K
ra	Reaction time @ 10 % of I _{PN}	< 100	ns ns
a	Response time @ 90 % of I _{PN}	< 400	ns
li/dt	di/dt accurately followed	> 15	A/µs
	Frequency bandwidth (0 0.5 dB)	DC 100	kHz
	(- 0.5 1 dB)	DC 200	kHz
Ge	eneral data		
A	Ambient operating temperature	- 40 + 85	°C
s S	Ambient storage temperature	- 40 + 10	_
S	Insulating material group	III a	- 3
n	Mass	10	g
	Standards 7)	EN 50178	: 1997
		IEC 60950-1	
		12000001	. 2001

Features

- Closed loop (compensated) multirange current transducer using the Hall effect
- Unipolar voltage supply
- Insulated plastic case recognized according to UL 94-V0
- Compact design for PCB mounting
- Incorporated measuring resistance
- Extended measuring range
- Access to the internal voltage reference
- Possibility to feed the transducer reference from external supply.

Advantages

- Excellent accuracy
- Very good linearity
- Very low temperature drift
- Optimized response time
- Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability.

Applications

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

Application domain

• Industrial.

Copyright protected.

Notes: see overleaf.

060315/8



Current Transducer LTSR 6-NP

Isolation characteristics					
V _d	R.m.s. voltage for AC isolation test, 50/60 Hz, 1 mn	3	kV		
$\hat{\mathbf{V}}_{d}$	Impulse withstand voltage 1.2/50 µs	> 8	kV		
		Min			
$\mathbf{V}_{_{\mathrm{e}}}$	R.m.s. voltage for partial discharge extinction @ 10pC	> 1.5	kV		
		Min			
dCp	Creepage distance 8)	15.35	m m		
dCl	Clearance distance 9)	6.2	m m		
CTI	Comparative Tracking Index (Group III a)	175			

Application examples

According to EN 50178 and IEC 61010-1 standards and following conditions:

- Over voltage category OV 3
- Pollution degree PD2

Non-uniform field	EN 50178	IEC 61010-1	
dCp, dCl, $\hat{\boldsymbol{V}}_{w}$	Rated isolation voltage	Nominal voltage	
Single isolation	600 V	600 V	
Reinforced isolation	300 V	300 V	

Notes: 1) Only in refout mode or with external REF less than 2.525 V and greater than 2.475 V. For external REF out of these limits see leaflet.

- $^{2)}$ \mathbf{V}_{OUT} is linked to \mathbf{V}_{REF} , by conception the difference between these two nodes for $\mathbf{I}_{p} = 0$ is maximum ± 25 mV, $2.475 \text{ V} < \mathbf{V}_{\text{OUT}} < 2.525 \text{ V}$.
- $^{3)}$ In Refout mode at $\boldsymbol{T}_{A}\!=\!25^{\circ}\text{C},\,2.475\,\text{V}\!<\boldsymbol{V}_{\text{REF}}\!<\!2.525\,\text{V}.$

The minimal impedance loading the ref pin should be > 220 kW. Internal impedance = 600 W.

-

For most applications you need to buffer this output to feed it into an ADC for example.

- $^{4)}$ To overdrive the REF (1.9 V .. 2.7 V) max. \pm 1 mA is needed.
- $^{5)}$ Please see the operation principle on the other side. $^{6)}$ Only due to TCR $_{\rm IM}$.
- ⁷⁾ Specification according to IEC 1000-4-8 not adhered to in DC, error according to two axes 1.5% instead of 1%. ⁸⁾ On housing.
- $^{9)}$ On PCB with soldering pattern UTEC93-703.

Safety



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.

Caution, risk of electrical shock



When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a built-in device, whose conducting parts must be inaccessible after installation.

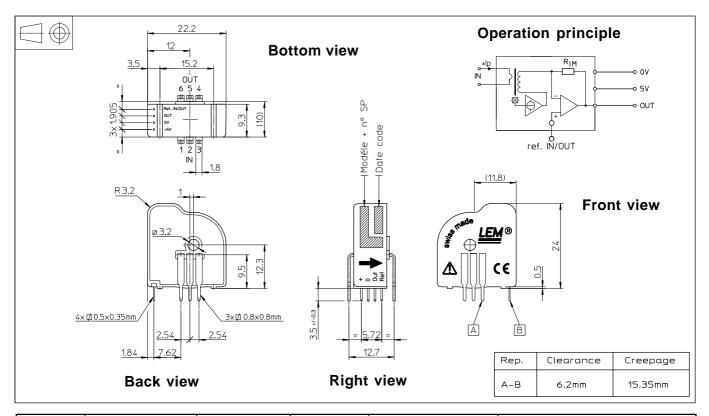
A protective housing or additional shield could be used.

Main supply must be able to be disconnected.

060315/8



Dimensions LTSR 6-NP (in mm. 1 mm = 0.0394 inch)



Number of primary turns	Primary nominal r.m.s. current I _{PN} [A]	Nominal 10) output voltage V _{OUT} [V]	Primary resistance \mathbf{R}_{P} [$\mathrm{m}\Omega$]	Primary insertion inductance L _P [µH]	Recommended connections
1	± 6	2.5 ± 0.625	0.18	0.013	6 5 4 OUT O
2	± 3	2.5 ± 0.625	0.81	0.05	6 5 4 OUT O O O IN 1 2 3
3	± 2	2.5 ± 0.625	1.62	0.12	6 5 4 OUT 0 0 IN 1 2 3

Mechanical characteristics

• General tolerance ±0.2 mm

• Fastening & connection of primary 6 pins 0.8 x 0.8 mm

Recommended PCB hole 1.3 mm
• Fastening & connection of secondary 4 pins 0.5 x 0.35 mm

Recommended PCB hole 0.8 mm

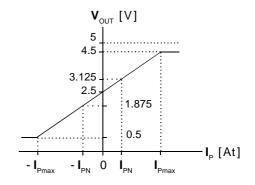
• Additional primary through-hole ∅3.2 mm

Remarks

- V_{OUT} is positive when I_p flows from terminals 1, 2, 3 to terminals 6, 5, 4
- For the EMC, the acceptance criteria are available on request
- Temperature of the primary jumper should not exceed 100 [°C].

Note: 10) Output voltage when LTSR 6-NP is used with internal reference.

Output Voltage - Primary Current



060315/8

www.lem.com

Pa