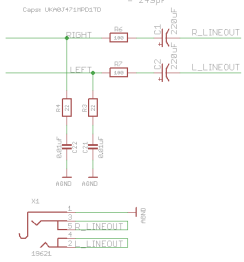
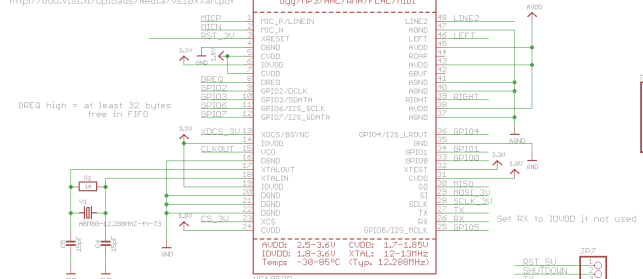


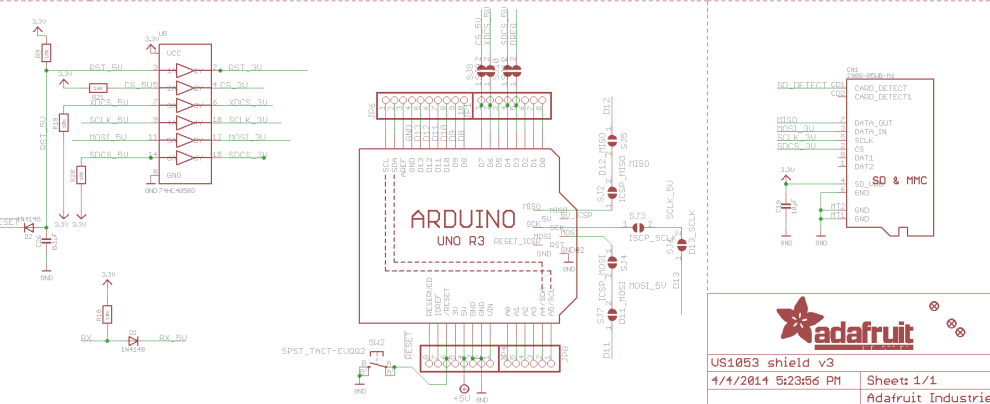
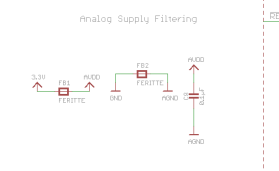
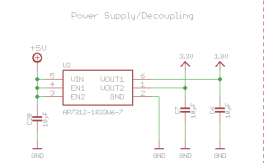
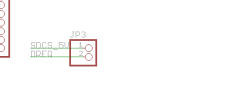
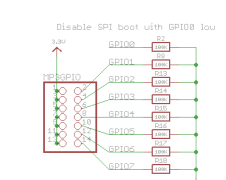
$C1, C2 = 1 / (2\pi f R K K \omega)$   
 16 ohm load at 20Hz cutoff =  $1 / (6.2832 * 20 * 16) = 497pF$   
 32 ohm load at 20Hz cutoff =  $1 / (6.2832 * 20 * 32) = 249pF$



Enabling the IC too soon can cause transients during power up which can damage the earphones and causes 'clicks' - this setup should minimize that. See section 5 of <http://www.vlsi.lt/uploads/media/vs10XXan.pdf>

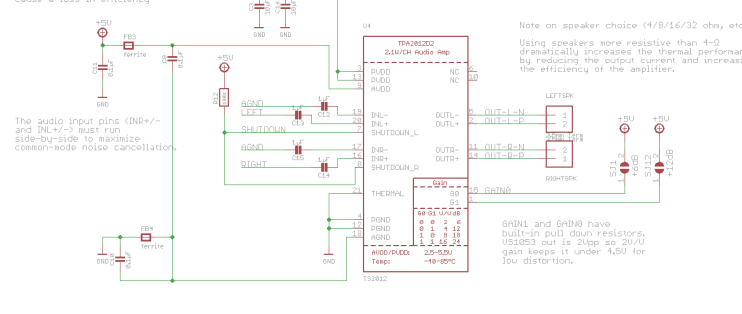


DREQ high = at least 32 bytes free in FIFO  
 VS1053 oscillator requires 1M resistor for DC bias  
 C4,C5 = 2\*CL = 2\*Ctray  
 C4,C5 = 2\*10pF = 2\*5pF  
 C4,C5 = 10pF



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Place C1 as close as possible to PUDD.  
 Any resistance or inductance in the trace between the device and the capacitor can cause a loss in efficiency.



Note on speaker choice (4/8/16/32 ohm, etc.):  
 Using speakers more resistive than 4-8 dramatically increases the thermal performance by reducing the output current and increasing the efficiency of the amplifier.  
 GAIN1 and GAIN0 have built-in pull down resistors. VS1053 out is 2Vpp so 2V/0 gain keeps it under 1.5V for low distortion.